

REMARKS

Applicants thank the Examiner for the total consideration given the present application. Claims 1-6 remain pending. Claims 5 and 6 have been amended through this Reply. Claims 1-6 are independent. Favorable reconsideration and allowance of the present application are respectfully requested in view of the following remarks.

ALLOWABLE SUBJECT MATTER

Applicants appreciate that claims 1-4 are indicated as allowable.

35 U.S.C. § 103 REJECTION – SHIN IN VIEW OF MASUDA ET AL.

Claims 5 and 6 stand rejected under 35 U.S.C. § 103(a) as allegedly being unpatentable over Shin (U.S. Patent 5,781,185) (hereinafter “Shin”) in view of Masuda et al. (U.S. Patent 5,978,041) (hereinafter “Masuda”). Applicants respectfully traverse.

For a Section 103 rejection to be proper, a *prima facie* case of obviousness must be established. *See M.P.E.P. 2142*. One requirement to establish *prima facie* case of obviousness is that the prior art references, when combined, must teach or suggest all claim limitations. *See M.P.E.P. 2142; M.P.E.P. 706.02(j)*. Thus, if the cited references fail to teach or suggest one or more elements, then the rejection is improper and must be withdrawn.

In this instance, claim 5 recites “A device for converting images, comprising: ... a controller for generating a control signal for controlling a cycle of the output horizontal synchronizing signal based on the input horizontal synchronization signal, the input vertical

synchronization signal, cycle information of the input horizontal synchronization signal, and cycle information of the asynchronous clock signal;” Emphasis added.

In contrast, Shin teaches an image display device that performs automatic display mode detection and centering display based on the display mode (col. 1, lines 46-52). The device includes first and second synchronization detecting units 10 and 20 that receive a vertical synchronization signal and a horizontal synchronization signal. Both synchronization detecting units 10 and 20 receive a clock signal MCLK. Shin’s teaches that the MCLK is used to eliminate high-end noise of each vertical and horizontal signal (col. 2, line 54 –col. 3, line 5).

However, Shin fails to teach “*a controller for generating a control signal for controlling a cycle of the output horizontal synchronizing signal based on the input horizontal synchronization signal, the input vertical synchronization signal, cycle information of the input horizontal synchronization signal, and cycle information of the asynchronous clock signal,*” as recited in claim 5. More specifically, Shin fails to teach or suggest that the input horizontal synchronization signal, the input vertical synchronization signal, cycle information of the input horizontal synchronization signal, and cycle information of the asynchronous clock signal are used to control the cycle of the output horizontal synchronizing signal.

In addition, Masuda teaches controlling output frequency is based on a timing control circuit 77 and an asynchronous clock signal generated by an asynchronous clock generator 732 (col. 64, line 50 –col. 45 line 27). However, Masuda fails to teach that the input horizontal synchronization signal, the input vertical synchronization signal, cycle information of the input horizontal synchronization signal, and cycle information of the asynchronous clock signal are used to control the cycle of the output horizontal synchronizing signal.

Neither Shin nor Masuda alone, or in combination, teach or suggest every single feature in claim 5. . Similarly, claim 6 includes some of the features in claim 5. Therefore, for at least these reasons, claims 5 and 6 are distinguishable from the combination of Shin and Masuda. Applicants respectfully request that the rejection of claims 5 and 6, based on Shin and Masuda, be withdrawn.

Conclusion

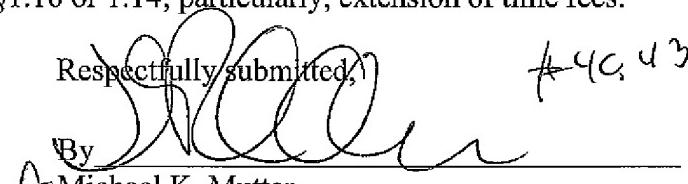
In view of the above remarks, it is believed that the claims are allowable.

Should there be any outstanding matters that need to be resolved in the present application, the Examiner is respectfully requested to contact Michael K. Mutter, Reg. No. 29,680, at the telephone number of the undersigned below, to conduct an interview in an effort to expedite prosecution in connection with the present application.

If necessary, the Commissioner is hereby authorized in this, concurrent, and future replies to charge payment or credit any overpayment to Deposit Account No. 02-2448 for any additional fees required under 37.C.F.R. §§1.16 or 1.14; particularly, extension of time fees.

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Respectfully submitted,


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